

Features:

- ✧ Up to 11.3Gb/s bit rates
- ✧ Hot-pluggable SFP+ footprint
- ✧ Single LC for Bi-directional Transmission
- ✧ Up to 80km link on SMF
- ✧ Built-in 1490 WDM Filter
- ✧ Built-in 1490nm CWDM EML Laser.
- ✧ Power dissipation <1.8W
- ✧ Built-in digital diagnostic functions

- ✧ Operating temperature range:0 ~ 70° C
- ✧ Very low EMI and excellent ESD protection
- ✧ RoHS Compliant

Applications:

- ✧ 10G Base-BX
- ✧ 10G SONET/SDH, OTU2/2e

Description:

Opway'OP5980D-45 Bi-directional 10Gb/s (SFP+) transceivers are compliant with the current SFP+ Multi-Source Agreement (MSA) Specification. They comply with 10GBASE-ZR/ZW Ethernet, SONET OC-192/SDH STM-64 and 10G Fibre Channel. Digital diagnostics functions are available via a 2-wire serial interface, as specified in the SFP+ MSA.

Specification:

● **Absolute Maximum Ratings**

Parameter	Symbol	Min.	Typical	Max.	Unit
Storage Temperature	T _S	-40		+85	°C
Supply Voltage	V _{CC} T, R	-0.3		4	V
Relative Humidity	RH	5		95	%

● **Recommended Operating Environment:**

Parameter	Symbol	Min.	Typical	Max.	Unit
Case Operating Temperature	T _C	0		+70	°C
Supply Voltage	V _{CC} T, R	+3.135		+3.465	V
Supply Current	I _{CC}			545	mA
Power Dissipation	P _D			1.8	W

● **Electrical Characteristics**

Parameter	Symbol	Min	Typ	Ma	Unit	Note
Transmitter:						
Differential Data Input Voltage	VDT	180	-	1200	mV	
Differential Line Input Impedance	RIN	80	100	120	Ω	
Transmitter Fault Output-High	VFaultH	2		V _{CC} +0.3	V	
Transmitter Fault Output-Low	VFaultL	-0.3		0.8	V	
Transmitter Disable Voltage- High	VDisH	2		V _{CC} +0.3	V	
Transmitter Disable Voltage- Low	VDisL	-0.3		0.8	V	
Receiver						

Differential Output Voltage Swing	VDR	300		850	mV	
Differential Line Output Impedance	ROUT	80	100	120	Ω	
Receiver LOS Pull up Resistor	RLOS	4.7		10	k Ω	
LOS Assert Leve	VLOS fault	Vcc-1.3		VccHOST	V	
LOS De-assert Level	VLOS norm	Vee		Vee+0.8	V	

● **Optical Characteristics**

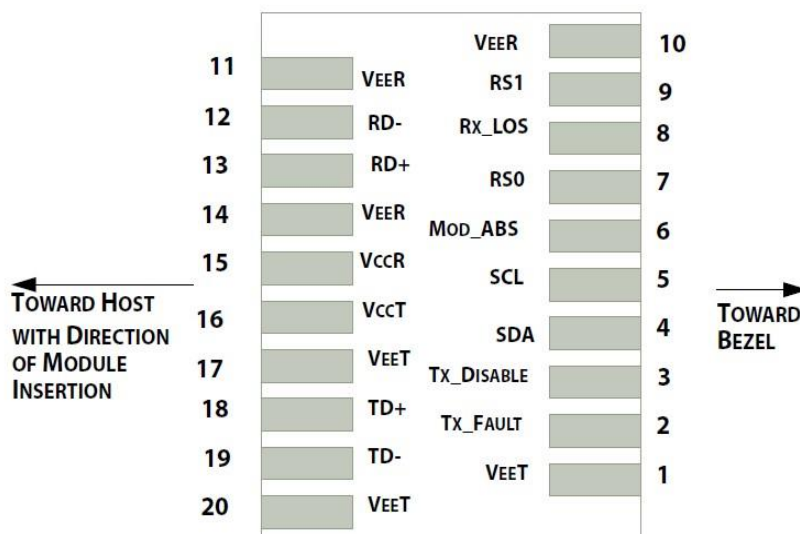
Paramete	Symbol	Min	Typ	Max	Unit	Ref.
Transmitter						
Date Rate		-	10.3125	11.3	Gb/s	
Optical Wavelength	λ	1480	1490	1500	nm	
Average Output Power	Po	0		+5	dBm	
Optical Extinction Ratio	ER	8.2			dB	
RMS spectral width	$\Delta\lambda$			0.3	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Optical Eye Mask		Compliant with IEEE802.3ae				
Receiver						
Date Rate		-	10.3125	11.3	Gb/s	
Optical Wavelength	λ	1533	1550	1560	nm	
Receiver Sensitivity@10G	SEN			-23	dBm	1
Input Saturation Power(Overload)	Psat	-7			dBm	
LOS De-Assert	LOSD			-24	dBm	
LOS Assert	LOSA	-38			dBm	
LOS Hysteresis		0.5			dB	

Notes:

Note 1) 1. Measured with worst ER, BER less than 1E-12 and PRBS 2^31-1 at 10.3125Gbps.

● **Pin Assignment:**

Diagram of Host Board Connector Block Pin Numbers and Name



● Pin Function Definitions

PIN #	Name	Function	Notes
1	VeeT	Module transmitter ground	1
2	Tx Fault	Module transmitter fault	2
3	Tx Disable	Transmitter Disable; Turns off transmitter laser output	3
4	SDL	2 wire serial interface data input/output (SDA)	
5	SCL	2 wire serial interface clock input (SCL)	
6	MOD-ABS	Module Absent, connect to VeeR or VeeT in the module	2
7	RS0	Rate select 0, optionally control SFP+ receiver. When high, input data rate >4.5Gb/s; when low, input data rate <=4.5Gb/s	
8	LOS	Receiver Loss of Signal Indication	4
9	RS1	Rate select0, optionally control SFP+ transmitter. When high, input data rate >4.5Gb/s; when low, input data rate <=4.5Gb/s	
10	VeeR	Module receiver ground	1
11	VeeR	Module receiver ground	1
12	RD-	Receiver inverted data out put	
13	RD+	Receiver non-inverted data out put	
14	VeeR	Module receiver ground	1
15	VccR	Module receiver 3.3V supply	
16	VccT	Module transmitter 3.3V supply	
17	VeeT	Module transmitter ground	1
18	TD+	Transmitter inverted data out put	
19	TD-	Transmitter non-inverted data out put	
20	VeeT	Module transmitter ground	1

Note 1) The module ground pins shall be isolated from the module case.

Note 2) This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.

Note 3) This pin shall be pulled up with 4.7K-10Kohms to VccT in the module.

Note 4) This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.

● SFP Module EEPROM Information and Management

The SFP+ modules implement the 2-wire serial communication protocol as defined in the SFP -8472. The serial ID information of the SFP modules and Digital Diagnostic Monitor parameters can be accessed through the I2C interface at address A0h and A2h.

The memory is mapped in Table 1.

Detailed ID information(A0h) is listed in Table 2.

And the DDM specification at address A2h.

For more details of the memory map and byte definitions, please refer to the SFF-8472, “Digital Diagnostic Monitoring Interface for Optical Transceivers”. The DDM parameters have been internally calibrated.

Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)

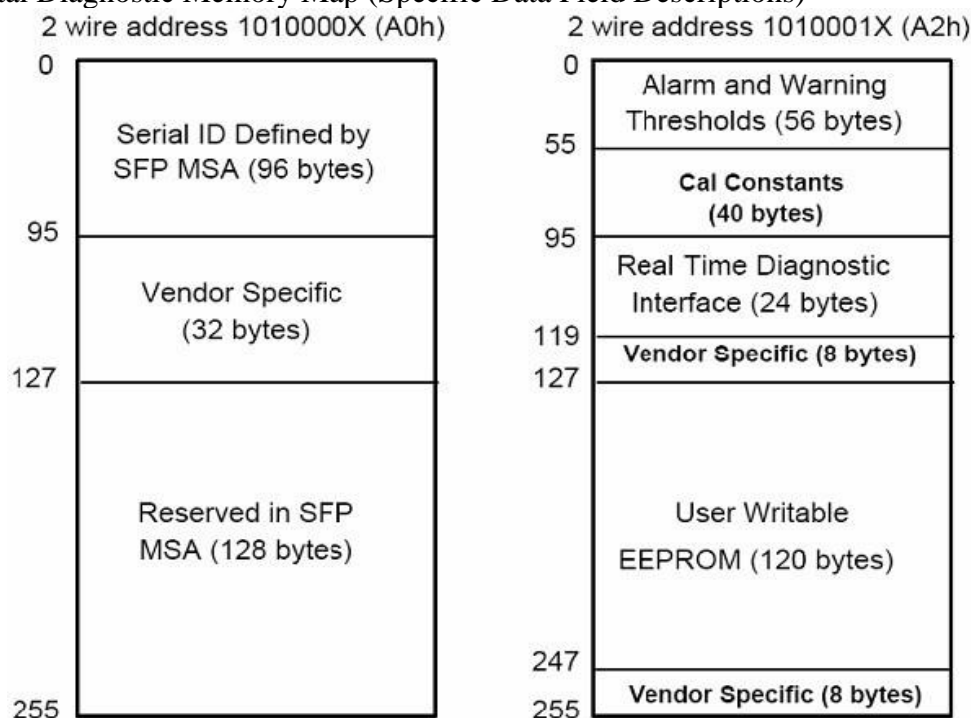


Table 2 - EEPROM Serial ID Memory Contents (A0h)

Data Address	Length (Byte)	Name of Length	Description and Contents
Base ID Fields			
0	1	Identifier	Type of Serial transceiver (03h=SFP+)
1	1	Reserved	Extended identifier of type serial transceiver
2	1	Connector	Code of optical connector type
3-10	8	Transceiver	
11	1	Encoding	64B/66B
12	1	BR, Nominal	Nominal baud rate, unit of 100Mbps
13-14	2	Reserved	(0000h)
15	1	Length(9um)	Link length supported for 9/125um fiber, units of 100m
16	1	Length(50um)	Link length supported for 50/125um fiber, units of 10m
17	1	Length(62.5um)	Link length supported for 62.5/125um fiber, units of 10m
18	1	Length(Copper)	Link length supported for copper, units of meters
19	1	Reserved	
20-35	16	Vendor Name	SFP vendor name: OPWAY
36	1	Reserved	
37-39	3	Vendor OUI	SFP transceiver vendor OUI ID
40-55	16	Vendor PN	Part Number: "OP5980D-45" (ASCII)
56-59	4	Vendor rev	Revision level for part number

60-62	3	Reserved	
63	1	CCID	Least significant byte of sum of data in address 0-62
Extended ID Fields			
64-65	2	Option	Indicates which optical SFP signals are implemented (001Ah = LOS, TX_FAULT, TX_DISABLE all supported)
66	1	BR, max	Upper bit rate margin, units of %
67	1	BR, min	Lower bit rate margin, units of %
68-83	16	Vendor SN	Serial number (ASCII)
84-91	8	Date code	OPWAY's Manufacturing date code
92-94	3	Reserved	
95	1	CCEX	Check code for the extended ID Fields (addresses 64 to 94)
Vendor Specific ID Fields			
96-127	32	Readable	OPWAY specific date, read only
128-255	128	Reserved	Reserved for SFF-8079

● **Digital Diagnostic Monitor Characteristics**

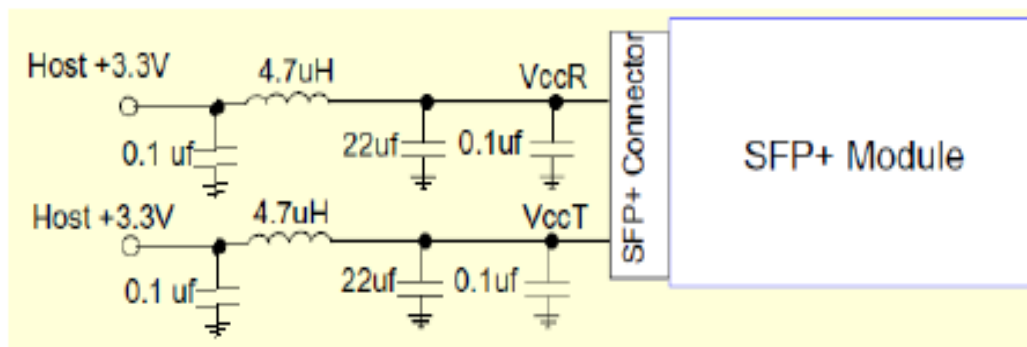
Data Address	Parameter	Accuracy	Unit
96-97	Transceiver Internal Temperature	±3.0	°C
98-99	VCC3 Internal Supply Voltage	±3.0	%
100-101	Laser Bias Current	±10	%
102-103	Tx Output Power	±3.0	dB
104-105	Rx Input Power	±3.0	dB

● **Regulatory Compliance**

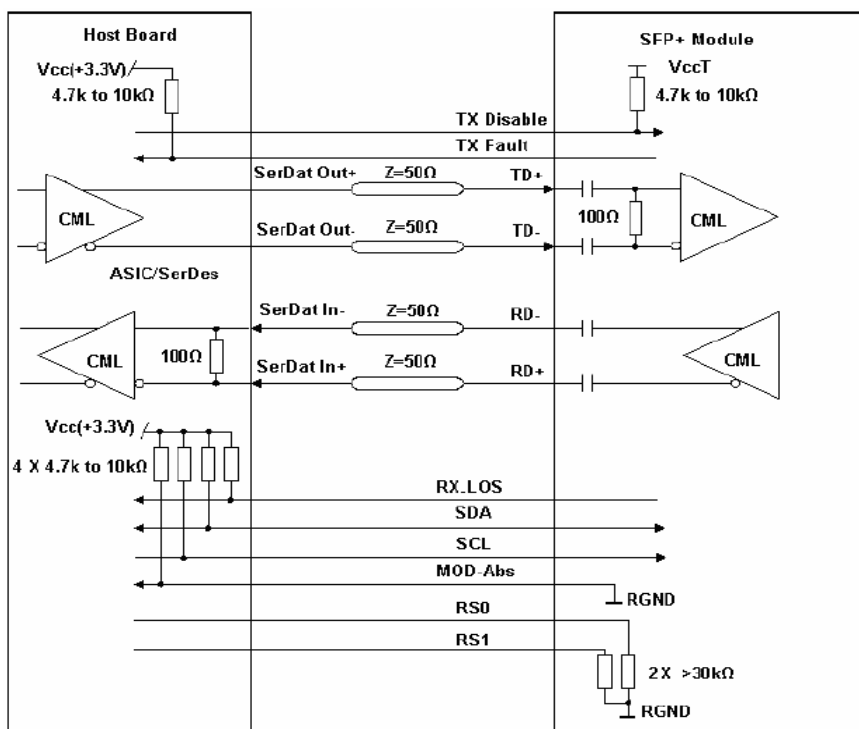
The OP5980D-45 complies with international Electromagnetic Compatibility (EMC) and international safety requirements and standards (see details in Table following).

Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>1000 V)
Electrostatic Discharge (ESD) to the Duplex LC Receptacle	IEC 61000-4-2 GR-1089-CORE	Compatible with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022 Class B (CISPR 22B) VCCI Class B	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN60950, EN (IEC) 60825-1,2	Compatible with Class 1 laser product.

● **Recommended Circuit:**

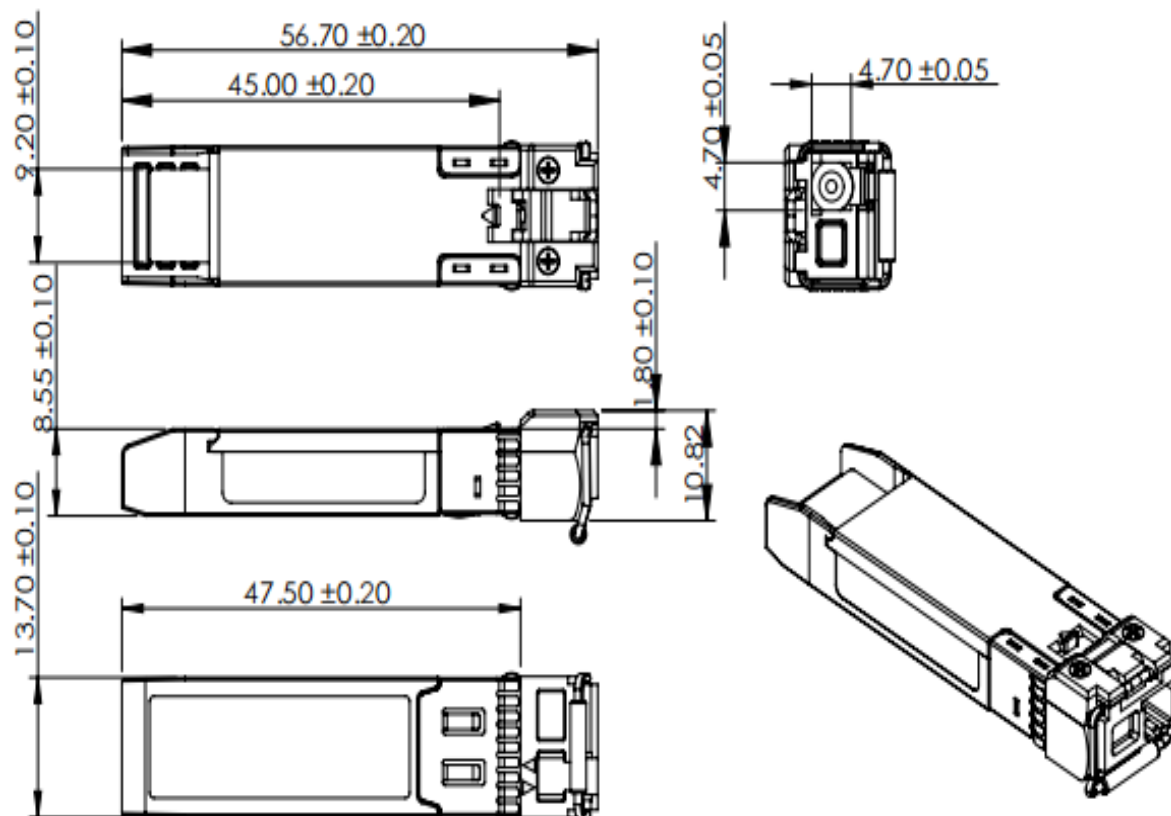


Recommended Host Board Power Supply Circuit



Recommended High-speed Interface Circuit

● **Mechanical Dimensions:**



OPWAY reserves the right to make changes to the products or information contained herein without notice. No liability is assumed as a result of their use or application. No rights under any patent accompany the sale of any such products or information.

Published by Shenzhen OPWAY Communication Co., Ltd.

Copyright © OPWAY

All Rights Reserved