



- ✧ 850nm VCSEL laser transmitter
- ✧ Duplex LC connector
- ✧ RoHS compliant
- ✧ 100m over 50/125µm MMF (50/125µm OM3)
- ✧ Metal enclosure, for lower EMI
- ✧ Single 3.3V power supply
- ✧ Operating temperature range: 0°C to 70°C
- ✧ RoHS compliant

Features:

- ✧ Up to 14.025 Gb/s bi-directional data links
- ✧ Hot-pluggable SFP+ footprint
- ✧ Built-in digital diagnostic functions

Applications:

- ✧ Tri-Rate 4.25/8.5/14.025 Gb/s Fibre Channel

Description:

OPWAY's OP3F01D SFP+ transceivers are designed for use in Fibre Channel links up to 14.025 Gb/s data rate over multimode fiber. They are compliant with FC-PI-5 Rev. 6.00, SFF-8472 Rev 11.0 and SFF-8081, and compatible with SFF-8432 and applicable portions of SFF-8431 Rev. 4.1.

The high performance 850nm VCSEL transmitter and high sensitivity PIN receiver provide superior performance for Ethernet applications at up to 100m links.

Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472.

The fully SFP compliant form factor provides hot pluggability, easy optical port upgrades and low EMI emission.

● **Absolute Maximum Ratings**

Parameter	Symbol	Min.	Typical	Max.	Unit
Storage Temperature	T _S	-40		+85	°C
Case Operating Temperature	T _A	0		70	°C
Maximum Supply Voltage	V _{cc}	-0.5		4	V
Relative Humidity	RH	0		85	%

● **Electrical Characteristics (T_{OP} = 0 to 70 °C, V_{CC} = 3.0 to 3.60 Volts)**

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Supply Voltage	V _{cc}	3.135		3.465	V	
Supply Current	I _{cc}			300	mA	1
Power Consumption	P			1.5	W	
Transmitter Section:						
Input differential impedance	R _{in}		100		Ω	2
Differential input voltage swing	V _{in,pp}	90		800	mV	

Transmit Disable Voltage	V _D	2		V _{CC}	V	3
Transmit Enable Voltage	V _{EN}	V _{EE}		V _{EE} +0.8	V	
Receiver Section:						
Single ended data output swing	V _{out,pp}	185		425	mV	4
LOS Fault	V _{LOS} fault	2		V _{CC} HOST	V	5
LOS Normal	V _{LOS} norm	V _{EE}		V _{EE} +0.8	V	5
Power Supply Rejection	PSR	100			mV _{pp}	6
Deterministic Jitter						

Note:

1. With established link. The total power dissipation could exceed 1W when the module is trying to establish link at operating case temperature below 25°C
2. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
3. Or open circuit.
4. Into 100 ohms differential termination.
5. LOS is an open collector output. Should be pulled up with 4.7k – 10kohms on the host board. Normal operation is logic 0; loss of signal is logic 1. Maximum pull-up voltage is 5.5V.
6. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.

● **Optical Parameters(T_{OP} = 0 to 70°C, V_{CC} = 3.00 to 3.60 Volts)**

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Transmitter Section:						
Center Wavelength	λ _t	840	850	860	nm	
RMS spectral width	λ _{RMS}			0.59	nm	
Average Optical Power	P _{avg}	-7.8		+2	dBm	1
Optical Power OMA	4.25Gb/s	P _{oma}	-6.1		dBm	
	8.5Gb/s		-5.2			
	14.025Gb/s		-4.8			
Optical Rise/Fall Time @ 4.25Gb/s	t _r / t _f			90	ps	2
Transmitter Waveform and Dispersion Penalty, 8.5 Gb/s	TWDP			4.3	dB	3
Vertical Eye Closure Penalty						
14.025 Gb/s	CEVPQ					
Receiver Section:						
Center Wavelength	λ _r	770		860	nm	
Average Receiver Power	R _X MAX	0			dBm	
Receiver Stressed Sensitivity(OMA)	4.25Gb/s	Sen _{ST}		-12.0	dBm	4
	8.5Gb/s			-11.2		
	14.025Gb/s			-10.5		
Optical Return Loss		12			dB	
Los Assert	LOS _A	-30		-	dBm	
Los Dessert	LOS _D			-13	dBm	

Los Hysteresis	LOS _H	0.5			dB	
----------------	------------------	-----	--	--	----	--

Notes:

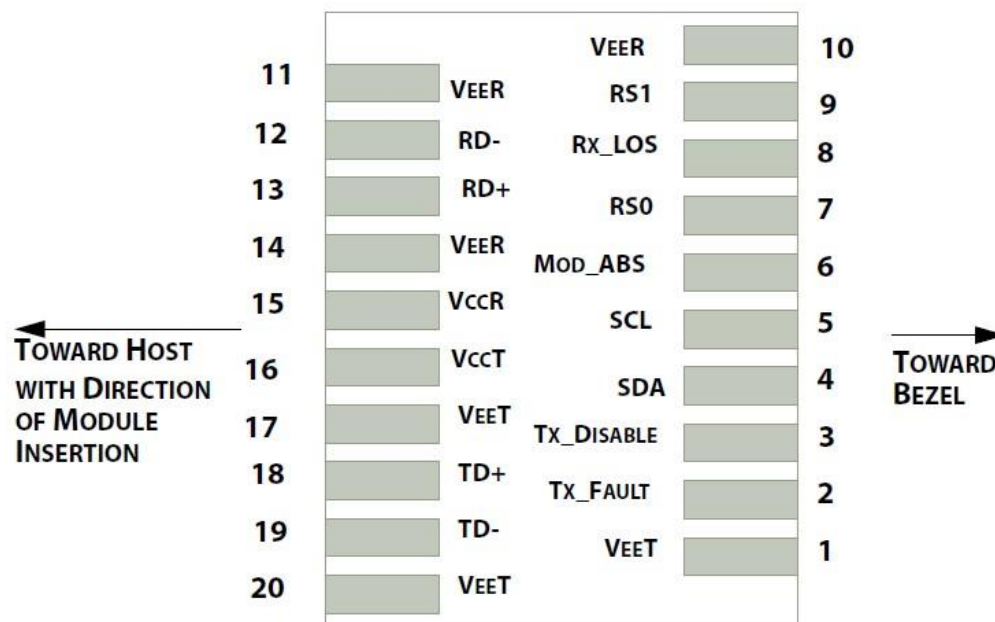
1. Class 1 Laser Safety limit per FDA/CDRH, and EN (IEC) 60825 laser safety standards.
2. Unfiltered, 20-80%. Complies with FC 1x and 2x eye mask when filtered
3. TWDP is calculated with a 1,0 equalizer and a 9.84 GHz Gaussian filter for the fiber simulation. Jitter values at γ_T and γ_R are controlled by TWDP and stress receiver sensitivity.
4. Unstressed Rx sensitivity

● Timing Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit
TX_Disable Assert Time	t_off			10	us
TX_Disable Negate Time	t_on			1	ms
Time to Initialize Include Reset of TX_FAULT	t_int			300	ms
TX_FAULT from Fault to Assertion	t_fault			100	us
TX_Disable Time to Start Reset	t_reset	10			us
Receiver Loss of Signal Assert Time	T _{A,RX_LOS}			100	us
Receiver Loss of Signal Deassert Time	T _{d,RX_LOS}			100	us
Rate-Select Change Time	t_ratesel			10	us
Serial ID Clock Time	t_serial-clock			100	kHz

● Pin Assignment

Diagram of Host Board Connector Block Pin Numbers and Name



● Pin Function Definitions

PIN	Symbol	Name / Description	Note
1	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1
2	T _{FAULT}	Transmitter Fault.	2

3	TDIS	Transmitter Disable. Laser output disabled on high or open.	3
4	SDA	2-wire Serial Interface Data Line (MOD-DEF2)	4
5	SCA	2-wire Serial Interface Clock (MOD-DEF1)	4
6	MOD_ABS	Module Absent, connected to VEET or VEER	4
7	RS0	Receiver Rate Select	
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	5
9	RS1	Transmitter Rate Select (not used)	
10	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1
11	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1
15	V _{CCR}	Receiver Power Supply	
16	V _{CCT}	Transmitter Power Supply	
17	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1

Notes:

1. Circuit ground is internally isolated from chassis ground.
2. TFAULT is an open collector/drain output, which should be pulled up with a 4.7k – 10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to Vcc + 0.3V. A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
3. Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
4. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. MOD_ABS pulls line low to indicate module is plugged in.
5. LOS is open collector output. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

● **SFP Module EEPROM Information and Management**

The SFP modules implement the 2-wire serial communication protocol as defined in the SFP -8472. The serial ID information of the SFP modules and Digital Diagnostic Monitor parameters can be accessed through the I²C interface at address A0h and A2h.

The memory is mapped in Table 1.

Detailed ID information (A0h) is listed in Table 2.

And the DDM specification is at address A2h.

For more details of the memory map and byte definitions, please refer to the SFF-8472, “Digital Diagnostic Monitoring Interface for Optical Transceivers”. The DDM parameters have been internally calibrated.

Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)

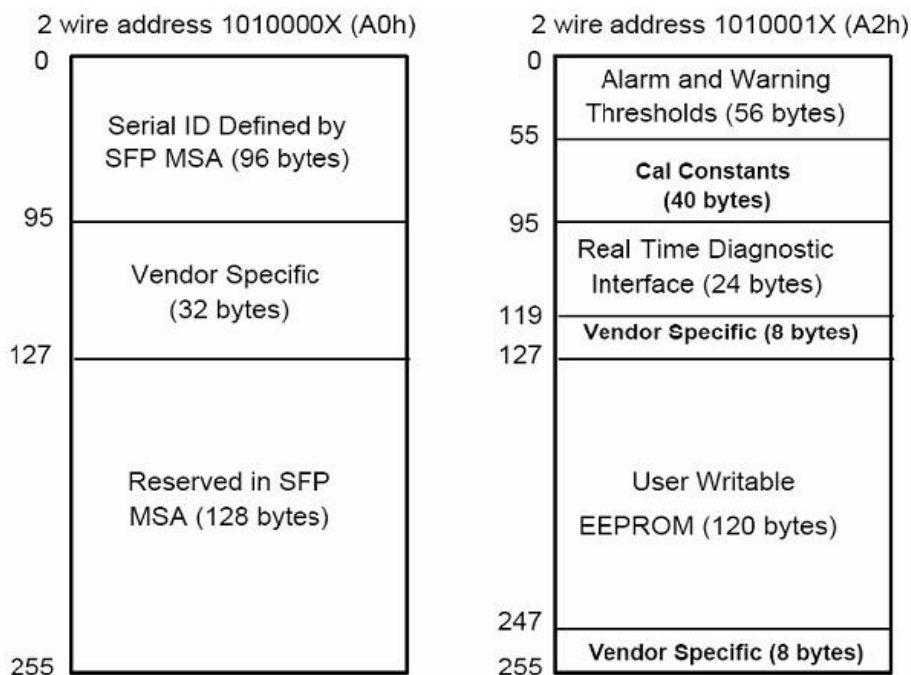


Table 2 - EEPROM Serial ID Memory Contents (A0h)

Data Address	Length (Byte)	Name of Length	Description and Contents
Base ID Fields			
0	1	Identifier	Type of Serial transceiver (03h=SFP)
1	1	Reserved	Extended identifier of type serial transceiver (04h)
2	1	Connector	Code of optical connector type (07=LC)
3-10	8	Transceiver	Fibre Channel SR Multimode
11	1	Encoding	64B/66B
12	1	BR, Nominal	Nominal baud rate, unit of 100Mbps
13-14	2	Reserved	(0000h)
15	1	Length(9um)	Link length supported for 9/125um fiber, units of 100m
16	1	Length(50um)	Link length supported for 50/125um fiber, units of 10m
17	1	Length(62.5um)	Link length supported for 62.5/125um fiber, units of 10m
18	1	Length(Copper)	Link length supported for copper, units of meters
19	1	Reserved	
20-35	16	Vendor Name	SFP vendor name: OPWAY
36	1	Reserved	
37-39	3	Vendor OUI	SFP transceiver vendor OUI ID
40-55	16	Vendor PN	Part Number: "OP3F01D" (ASCII)
56-59	4	Vendor rev	Revision level for part number
60-62	3	Reserved	
63	1	CCID	Least significant byte of sum of data in address 0-62
Extended ID Fields			
64-65	2	Option	Indicates which optical SFP signals are implemented (001Ah = LOS, TX_FAULT, TX_DISABLE all supported)
66	1	BR, max	Upper bit rate margin, units of %
67	1	BR, min	Lower bit rate margin, units of %

68-83	16	Vendor SN	Serial number (ASCII)
84-91	8	Date code	OPWAY's Manufacturing date code
92-94	3	Reserved	
95	1	CCEX	Check code for the extended ID Fields (addresses 64 to 94)
Vendor Specific ID Fields			
96-127	32	Readable	OPWAY specific date, read only
128-255	128	Reserved	Reserved for SFF-8079

Digital Diagnostic Specifications

OP3F01D transceivers can be used in host systems that require either internally or externally calibrated digital diagnostics.

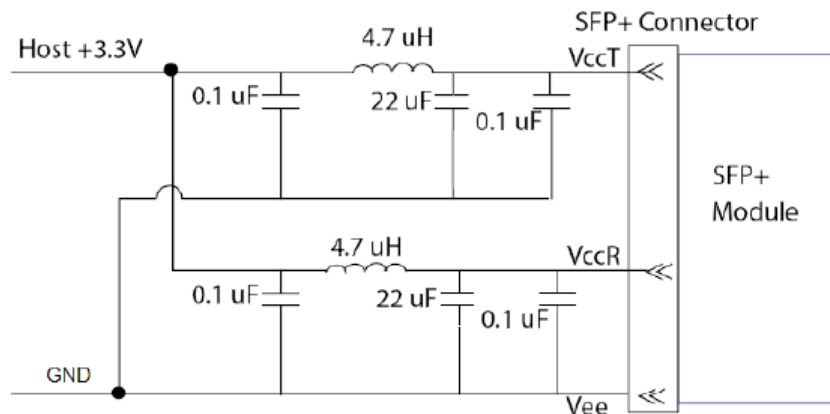
Parameter	Units	Accuracy
Transceiver temperature	°C	±5
Transceiver supply voltage	V	±3%
Transmitter bias current	mA	±10%
Transmitter output power	dBm	±3dB
Receiver average optical input power	dBm	±3dB

● Regulatory Compliance

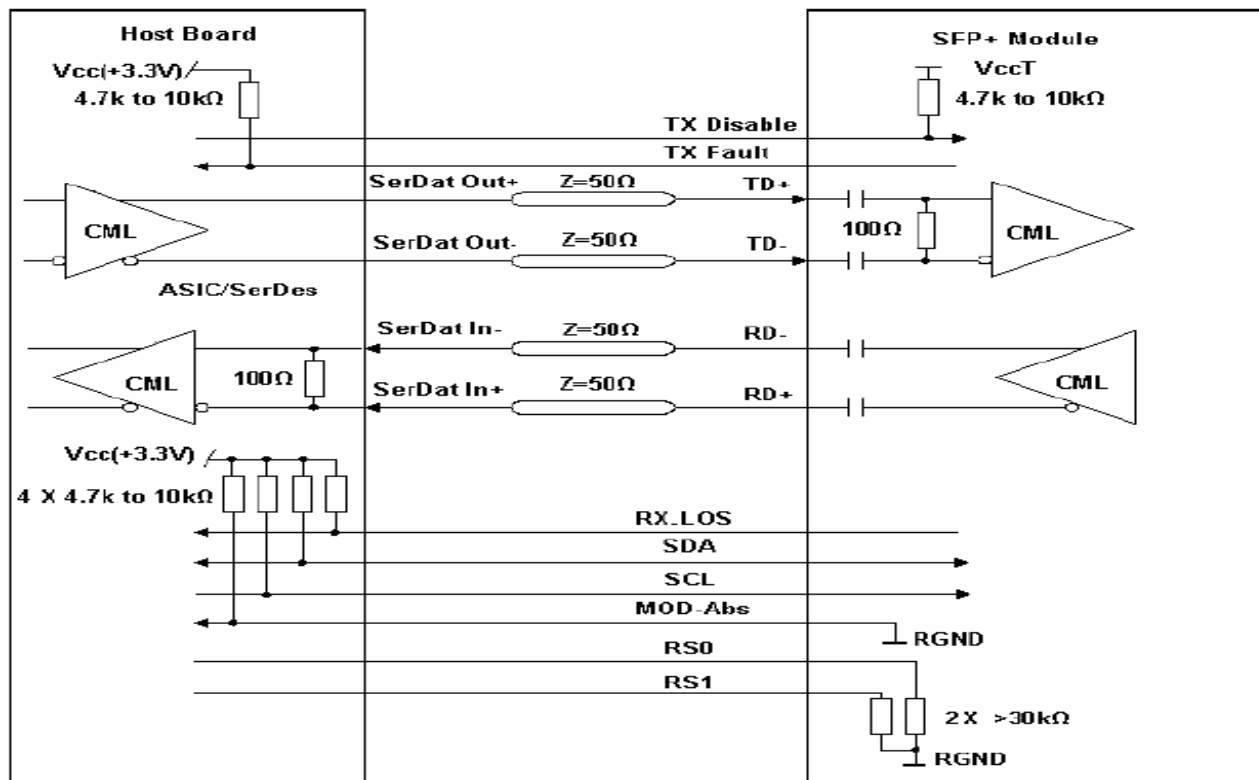
The OP3F01D complies with international Electromagnetic Compatibility (EMC) and international safety requirements and standards (see details in Table following).

Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>1000 V)
Electrostatic Discharge (ESD) to the Duplex LC Receptacle	IEC 61000-4-2 GR-1089-CORE	Compatible with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022 Class B (CISPR 22B) VCCI Class B	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN60950, EN (IEC) 60825-1,2	Compatible with Class 1 laser product.

● Recommended Circuit

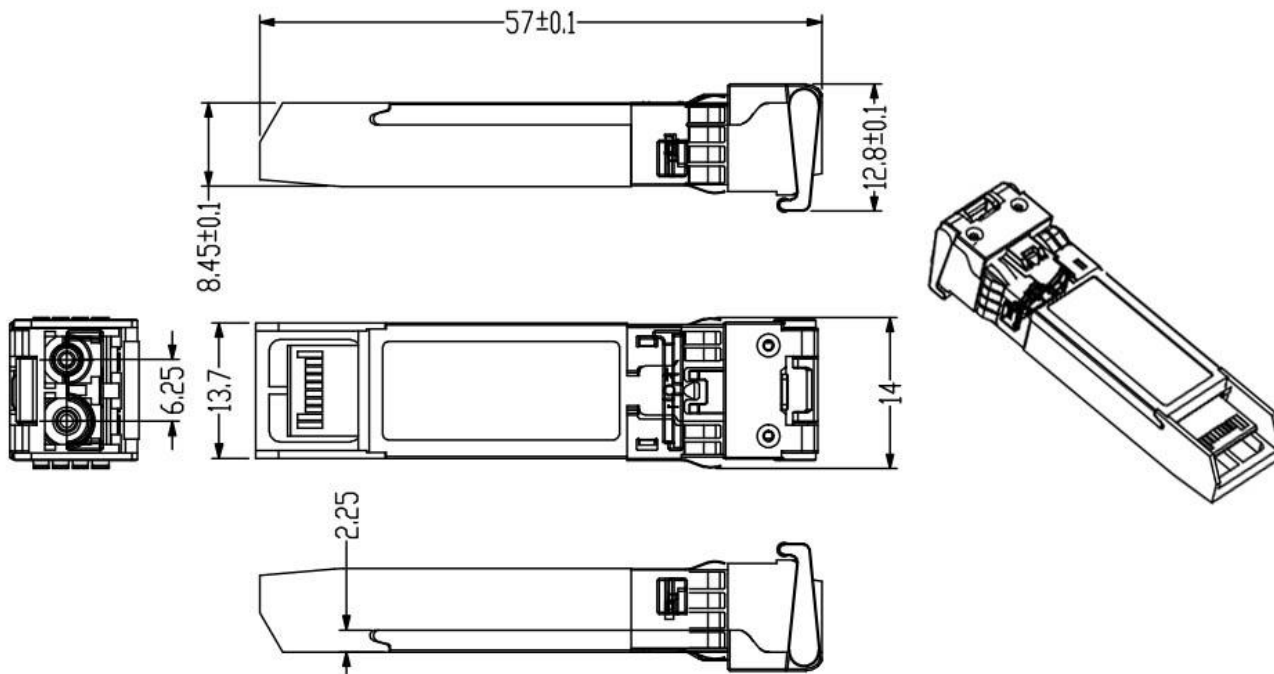


Recommended Host Board Power Supply Circuit



Recommended High-speed Interface Circuit

● **Mechanical Dimensions:**



OPWAY reserves the right to make changes to the products or information contained herein without notice. No liability is assumed as a result of their use or application. No rights under any patent accompany the sale of any such products or information.

Published by Shenzhen OPWAY Technology Co., Ltd.

Copyright © OPWAY

All Rights Reserved